



Sant Longowal Institute of Engineering & Technology
(Deemed to be University, Established by Government of India)
Longowal, District- Sangrur, Punjab- 148 106
Office of Dean (Research & Consultancy)

Ref. No. SLIET/Dean/(R&C)/2025/634

Dated: 14/08/2025

To

Mr. Sachin Tyagi (Registration No. PEC2504),
S/o Sh. Suresh Tyagi,
Department of Electronics and Communication Engineering,
SLIET, Longowal.


Subject: Admission to Part Time Ph.D. Programme under Visvesvaraya Ph.D. scheme for Electronics and IT: Phase-II (odd Semester 2025, Academic Year 2025-26) as Part Time Scholar.

Implementation Order No. PhD_PT-02/04-2025/31 dated: 03/04/2025.

Consequent upon the recommendations of Selection Committee, Counselling held on 08/08/2025 and the approval of Competent Authority, you are hereby admitted to **Ph.D. programme as Part Time Scholar without fellowship under Visvesvaraya Ph.D. scheme for Electronics and IT: II** in the Department of Electronics and Communication Engineering, subject to approval of the SENATE on following terms and conditions:

1. You will be governed by the "Ordinances, Rules and Regulations for Doctor of Philosophy (Ph.D.) Degree, 2018" of the Institute, as amended from time to time, Guidelines issued by Ministry of Education (MOE), Government of India, and Regulation of University Grants Commission for Ph.D. Scholars from time to time. And as per Implementation Order No. PhD_PT-02/04-2025/31 dated: 03/04/2025. You are required to give undertaking that you are agreed to follow terms and conditions and standard operating procedure of above said Visvesvaraya Ph.D. scheme and this admission letter.
2. In case, it is found at any stage during the course of study that the information furnished by you is incorrect or misleading or ineligibility is detected, your candidature will be cancelled.
3. Your progress of work and performance will be monitored after every six months by RPC/DRC. Any three unsatisfactory reports (half yearly) shall be treated as cancellation of Ph.D. registration. First unsatisfactory report of supervisor (s) / RPC / DRC will put the candidate on scholastic probation- I, second unsatisfactory report will put the Research Scholar on scholastic probation-II and so on.
4. In case you do not register yourself by the due dates in each semester, your registration shall be reviewed by the DRC and CRC. Further, if you fail to register on time second time, or does not register for two consecutive semesters during the course of study, your registration shall withstand cancelled.
5. You will be provided with hostel or a suitable accommodation subject to availability and the rent and other charges shall be applicable during the stay.
6. The offer of admission is **provisional** and can be withdrawn any time without assigning any reason.

You are required to report to the **Head of Electronics and Communication Engineering Department w.e.f. 18/08/2025** for further instructions.


(Prof. Surinder Singh)
Dean (R & C) and

Nodal Officer, Visvesvaraya Ph.D. scheme for Electronics and IT: Phase-II

Copy to:

1. Dean (Academics) for kind information
2. Concerned- HOD for kind information and necessary action
3. DR (A&A) for kind information and necessary action
4. File copy